REMARKS

Applicants appreciate the Examiner's thorough review of the present application, and respectfully request reconsideration in light of the preceding amendments and the following remarks.

Amendments to the Claims

Claims 1-2 and 6-18 are pending in the instant application.   Claims 10 and 11 have been amended to correctly depend on claim 9.   New claims 14-18 have been added to provide Applicants with the scope of protection to which they are believed entitled.   The amended/new claims find solid support in the original specification and the drawings, e.g., FIG. 4b and paragraphs 0042-0048 of the published application.

Discussion of Claim Rejections

Claim 1-2, 8, and 12:

Claim 1 is rejected under 35 U.S.C. 102(b) as being anticipated by *Nitta* (US 2001/0004257).   Applicant respectfully traverses the 35 U.S.C. 102(b) rejection not only for the reasons presented in the previous Amendments (which are incorporated by reference herein), but also for at least the following additional reasons.

Nitta **does not teach, disclose or suggest the display parameters belonging to a group consisting of: a number of lines to be displayed, a number of columns to be displayed, parameters related to driving transistors of the display device, and power saving parameters for the display device.**

The Office Action cited that, according to paragraph [0011], lines 4-8 of *Nitta*, *Nitta* discloses the display parameters belonging to a group consisting of: a number of lines to be displayed, a number of columns to be displayed, and parameters related to driving transistors of

the display device, and that, according to paragraph [0052], _Nitta_ discloses the group further comprises power saving parameters for the display device.

According to paragraph [0030], lines 5-6 of _Nitta_, the EDID storage memories 23 and 25 store EDID corresponding to the respective interface. EDID (Extended Display Identification Data) includes the resolution, frequency of vertical scan signals, frame rate, vender code indicating the manufacturer's name, and the serial number of the display apparatus, as shown in paragraph [0011]. Moreover, _Nitta_ does disclose the EDID stored in the EDID storage memories 23 and 25 comprises power saving parameters.

Paragraph [0052] of _Nitta_ cites that when power supply to the display 8 is completely cut off, except in the power save mode for reducing power consumption by the display 8, the power line Vcc1 becomes "0 V". However, although the display 8 has a power save mode, _Nitta_ does disclose parameters referring to the power save mode are stored in the EDID storage memories 23 and 25.

On the contrary, in the claim 1 of the application, the electronic apparatus comprises memory means for storing at least display parameters related to said application. The display parameters belongs to a group consisting of: a number of lines to be displayed, a number of columns to be displayed, parameters related to driving transistors of the display device, and **power saving parameters for the display device**. These features are not disclosed in _Nitta_.

Nitta does not teach, disclose or suggest a controller for selecting at least one application for the display device

The Examiner's interpretation of the prior art EDID (Extended Display Identification Data) as application (i.e., software, program) in the Office Action at page 3, lines 1-6 is simply wrong and unreasonable to a person of ordinary skill in the art who would at once recognize that data and application are not one and the same.

Nitta does not teach, disclose or suggest a memory means for storing at least display parameters <u>related to said application</u>

The Examiner's interpretation of the prior art EDID as the claimed display parameters in the Office Action at page 4, line 4 from bottom, is improper, because the prior art EDID is <u>related to the panel,</u> rather than to an application as presently claimed.

Thus, claim 1 and the respective dependent claims are patentable over _Nitta_.

Claim 1 is rejected under 35 U.S.C. 103(a) as being unpatentable over _Moon_ (US 6,085,098). Applicant respectfully traverses the 35 U.S.C. 103(a) rejection for at least the following reasons.

Moon does not teach, disclose or suggest means for providing said display parameters to an interface between the electronic apparatus and the display device.

The Office Action cited that the outputting means of the processing circuit 26 supposedly corresponds to means for providing said display parameters to an interface between the electronic apparatus and the display device.

According to column 3, line 61 to column4, line 4, the processing circuit 26 is connected to both Read Only Memory (ROM) 28 and Random Access Memory (RAM) 30 in which both operating systems and software applications are stored. The processing circuit 26 is also coupled to a display screen 22 through a standard driver (not shown) in order to control the images displayed thereon, as well as to receive information through graphical user interfaces in which the user of the portable intelligent communications device 10 may indicate chosen options. _Moon_ only discloses that operating systems and software applications are stored in the ROM 28 and RAM 28, and the processing circuit 26 is used to control the images displayed on the display screen 22. _Moon_ does not disclose that the processing circuit 26 provides the parameters stored in the ROM 28 and RAM 30 to the display screen 22. In FIG. 2, the ROM 28 and RAM 30 do not provide the stored parameters to the display screen 22.

On the contrary, in claim 1 of the application, the electronic apparatus comprises means for providing said display parameters to an interface between the electronic apparatus and the display device.   These features are not disclosed in *Moon*.

Moon **does not teach, disclose or suggest the display parameters belonging to a group consisting of: a number of lines to be displayed, a number of columns to be displayed, parameters related to driving transistors of the display device, and power saving parameters for the display device.**

The Office Action cites that LCD is well known for high native resolution and low power consumption.   However, according to column 3, line 61 to column 4, line 4, the ROM 28 and the RAM 30 store operating systems and software applications.   *Moon* does not specify parameters relating to power saving are stored in the ROM 28 and/or the RAM 30.

On the contrary, in the claim 1 of the application, the electronic apparatus comprises memory means for storing at least display parameters related to said application.   The display parameters belongs to a group consisting of: a number of lines to be displayed, a number of columns to be displayed, parameters related to driving transistors of the display device, and **power saving parameters for the display device.**   These features are not disclosed in *Moon*.

Thus, claim 1 and the respective dependent claims are patentable over *Moon*.

Claims 6-7:

Claim 6 is rejected under 35 U.S.C. 102(b) as being anticipated by *Nitta* (US 2001/0004257).   Applicant respectfully traverses the 35 U.S.C. 102(b) rejection for at least the following reason.

Nitta **does not teach, disclose or suggest the display device comprises means for recognizing an identification code at an interface between the electronic apparatus and the display device.**

The Office Action cites that a part of the multiplexer 31 controlling the signal transmission from the receptor 9 to the lines A0 or A1 supposedly corresponds to means for recognizing an identification code at an interface between the electronic apparatus and the display device.

According to paragraph [0030], lines 6-10 of *Nitta*, synchronously with a clock from a DDC clock line 27 received at serial clock terminals 23a and 25a, the memories 23 and 25 output the EDID from serial data terminals 23b and 25b to a DDC data line 29. In other words, the part of the multiplexer 31 connects DDC clock line 27 and passes clock from the DDC clock line 27 to the lines A0 or A1 according to the clock from the DDC clock line 27. The clock received by the multiplexer 31 is a signal for synchronization. The part of the multiplexer 31 receiving the clock can not be used to recognizing an identification code at an interface between the electronic apparatus and display device.

On the contrary, in the claim 6 of the application, the display device comprises means for recognizing an identification code at an interface between the electronic apparatus and the display device. These features are not disclosed in *Nitta*.

Claim 6 is rejected under 35 U.S.C. 103(a) as being unpatentable over *Nitta* (US 2001/0004257). Applicant respectfully traverses the 35 U.S.C. 103(a) rejection for at least the following reason.

Nitta **does not teach, disclose or suggest the display device comprises means for recognizing an identification code at an interface between the electronic apparatus and the display device.**

According to same reasons of the rejection under 35 U.S.C. 102(b), the part of the multiplexer 31 receiving the clock can not be used to recognizing an identification code at an interface between the electronic apparatus and display device. On the contrary, in the claim 6 of the application, the display device comprises means for recognizing an identification code at

an interface between the electronic apparatus and the display device.   These features are neither disclosed, taught nor suggested in *Nitta*.

It should be noted that *Nitta* does not teach or suggest the limitations of claim 6 that (i) a controller for selecting at least one <u>application</u> for the display device, and (ii) a memory means for storing at least display parameters <u>related to said application</u> as discussed above with respect to claim 1.

Thus, claim 6 and the respective dependent claim(s) are patentable over *Nitta*.

Claims 9-11 and 13-18:

Claim 9 is rejected under 35 U.S.C. 103(a) as being unpatentable over *Moon* (US 6,085,098).   Applicant respectfully traverses the 35 U.S.C. 103(a) rejection for at least the following reason.

***Moon* does not teach, disclose or suggest the display parameters belonging to a group consisting of: a number of lines to be displayed, a number of columns to be displayed, parameters related to driving transistors of the display device, and power saving parameters for the display device.**

The Office Action cites that LCD is well known for high native resolution and low power consumption.   However, according to column 3, line 61 to column, line 4, the ROM 28 and the RAM 30 store operating systems and software applications.   *Moon* does not specify parameters relating to power saving are stored in the ROM 28 and/or the RAM 30.

On the contrary, in the claim 9 of the application, the electronic apparatus comprises memory means for storing at least display parameters related to said application.   The display parameters belongs to a group consisting of: a number of lines to be displayed, a number of columns to be displayed, parameters related to driving transistors of the display device, and **power saving parameters for the display device.**   These features are not disclosed in *Moon*.

Moon **does not teach, disclose or suggest providing said display parameters from the electronic apparatus and the display device.**

According to column 3, line 61 to column, line 4, the processing circuit 26 is connected to both Read Only Memory (ROM) 28 and Random Access Memory (RAM) 30 in which both operating systems and software applications are stored. The processing circuit 26 is also coupled to a display screen 22 through a standard driver (not shown) in order to control the images displayed thereon, as well as receive information through graphical user interfaces in which the user of the portable intelligent communications device 10 may indicate chosen options. _Moon_ only discloses that operating systems and software applications are stored in the ROM 28 and RAM 28, and the processing circuit 26 is used to control the images displayed on the display screen 22. _Moon_ does not disclose the processing circuit 26 provides the parameters stored in the ROM 28 and RAM 30 to the display screen 22. In FIG. 2, the ROM 28 and RAM 30 do not provide the stored parameters to the display screen 22.

On the contrary, in the claim 9 of the application, the electronic apparatus comprises means for providing said display parameters to an interface between the electronic apparatus and the display device. These features are not disclosed in _Moon._

Thus, claim 9 and the respective dependent claim(s), including the new claims, are patentable over _Nitta._

The new dependent claims are also patentable on their own merits since these claims recite other features neither disclosed, taught nor suggested by the applied art, as will be apparent to the Examiner upon reviewing these claims. For example, none of the applied references fairly teach or suggest the claimed sequence, data blocks, dummy lines as recited in claims 14-18.
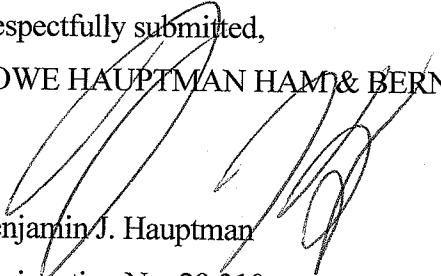
Conclusion

Each of the Examiner's rejections has been traversed. Accordingly, Applicants respectfully submit that all claims are now in condition for allowance. Early and favorable indication of allowance is courteously solicited.

The Examiner is invited to telephone the undersigned, Applicant's attorney of record, to facilitate advancement of the present application.

To the extent necessary, a petition for an extension of time under 37 C.F.R. 1.136 is hereby made. Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 07-1337 and please credit any excess fees to such deposit account.

Respectfully submitted,

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